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Hassler, H.; Takagi, N.;

Computer Arithmetic, 1995., Proceedings of the 12th Symposium on , 19-21 July 1995 Pages:10 - 16

[Abstract] [PDF Full-Text (516KB)] IEEE CNF

2 X-band ferrite phase shifter in waveguide geometry

Jong-Min Park; Dong-Chul Park;

TENCON '93. Proceedings. Computer, Communication, Control and Power Engineering.1993 IEEE Region 10 Conference on , Issue: 0 , 19-21 Oct. 1993 Pages:464 - 467 vol.3

[Abstract] [PDF Full-Text (236KB)] IEEE CNF

3 Arithmetic error analysis of a new reciprocal cell

Jain, V.K.; Perez, G.E.; Swartzlander, E.E., Jr.; Computer Design: VLSI in Computers and Processors, 1992. ICCD '92. Proceedings., IEEE 1992 International Conference on , 11-14 Oct. 1992 Pages:106 - 109

[Abstract] [PDF Full-Text (312KB)] IEEE CNF

4 ROM table reduction techniques for computing the squaring operation using modular arithmetic

Skavantzos, A.;

Signals, Systems and Computers, 1991. 1991 Conference Record of the Twenty-Fifth Asilomar Conference on , 4-6 Nov. 1991 Pages:413 - 417 vol.1

[Abstract] [PDF Full-Text (220KB)] IEEE CNF

5 Residue multipliers using factored decomposition Dugdale, M.:

Circuits and Systems II: Analog and Digital Signal Processing, IEEE Transactions on [see also Circuits and Systems II: Express Briefs, IEEE Transactions on], Volume: 41, Issue: 9, Sept. 1994
Pages:623 - 627

[Abstract] [PDF Full-Text (364KB)] IEEE JNL

6 A hardware cost minimized fast Phong shader

Hyun-Chul Shin; Jin-Aeon Lee; Lee-Sup Kim; Very Large Scale Integration (VLSI) Systems, IEEE Transactions on ,Volume: 9 , Issue: 2 , April 2001 Pages:297 - 304

[Abstract] [PDF Full-Text (228KB)] IEEE JNL

7 Design of a high-speed square generator

Chin-Long Wey; Ming-Der Shieh;

Computers, IEEE Transactions on ,Volume: 47 , Issue: 9 , Sept. 1998

Pages:1021 - 1026

[Abstract] [PDF Full-Text (260KB)] IEEE JNL

8 Novel Viterbi decoder VLSI implementation and its performance

Kubota, S.; Kato, S.; Ishitani, T.;

Communications, IEEE Transactions on ,Volume: 41 , Issue: 8 , Aug. 1993

Pages:1170 - 1178

[Abstract] [PDF Full-Text (688KB)] IEEE JNL

9 Comments on 'An architecture for addition and subtraction of long word length numbers in the logarithmic number system' by D.M. Lewis Arnold, M.; Bailey, T.; Cowles, J.; Computers, IEEE Transactions on ,Volume: 41 , Issue: 6 , June 1992

Pages:786 - 788

[Abstract] [PDF Full-Text (176KB)] IEEE JNL

10 Comments, with reply, on 'Procedure for counting moving objects as they stop' Ovaska. S.J.:

Instrumentation and Measurement, IEEE Transactions on .Volume: 39 . Issue: 2 . April 1990

Pages:445 - 446

[Abstract] [PDF Full-Text (80KB)] IEEE JNL

11 Combined unsigned and two's complement hybrid squarers

Walters, E.G., III; Schlessman, J.; Schulte, M.J.;

Signals, Systems and Computers, 2001. Conference Record of the Thirty-Fifth Asilomar Conference on ,Volume: 1 , 4-7 Nov. 2001 Pages:861 - 866 vol.1

[Abstract] [PDF Full-Text (423KB)] IEEE CNF

12 Hardware implementation of a nonlinear processor

Jain, V.K.; Shrivastava, S.; Snider, A.D.; Damerow, D.; Chester, D.; Circuits and Systems, 1999. ISCAS '99. Proceedings of the 1999 IEEE International Symposium on ,Volume: 6 , 30 May-2 June 1999 Pages:509 - 514 vol.6

[Abstract] [PDF Full-Text (384KB)] IEEE CNF

13 A minimized hardware architecture of fast Phong shader using Taylor series approximation in 3D graphics

Hyun-Chul Shin; Jin-Aeon Lee; Lee-Sup Kim;

Computer Design: VLSI in Computers and Processors, 1998. ICCD '98. Proceedings... International Conference on , 5-7 Oct. 1998

Pages:286 - 291

[Abstract] [PDF Full-Text (212KB)] IEEE CNF

14 Efficient algorithms for binary logarithmic conversion and addition

Yi Wan; Chin-Long Wey;

Circuits and Systems, 1998. ISCAS '98. Proceedings of the 1998 IEEE International Symposium on ,Volume: 5 , 31 May-3 June 1998 Pages:233 - 236 vol.5

[Abstract] [PDF Full-Text (320KB)] IEEE CNF

15 Nonlinear DSP coprocessor cells-one and two cycle chips

Jain, V.K.; Lin, L.;

Circuits and Systems, 1998. ISCAS '98. Proceedings of the 1998 IEEE International Symposium on ,Volume: 2 , 31 May-3 June 1998 Pages:264 - 267 vol.2

[Abstract] [PDF Full-Text (376KB)] IEEE CNF

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16 Latest development of current-fed three phase converter with ROM table-based optimum PWM control scheme and its input line filter design

Konishi, Y.; Matsumoto, Y.; Feng, Y.L.; Nakaoka, M.,

Power Électronics Specialists Conference, 1997. PESC '97 Record., 28th Annual IEEE ,Volume: 2, 22-27 June 1997

Pages:813 - 820 vol.2

[Abstract] [PDF Full-Text (628KB)] IEEE CNF

17 Postdetection maximal ratio combining diversity receiver with scalar phase signals

Kumagai, T.; Kobayashi, K.;

Universal Personal Communications Record, 1997. Conference Record., 1997 IEEE 6th International Conference on ,Volume: 2 , 12-16 Oct. 1997

Pages:391 - 395 vol.2

[Abstract] [PDF Full-Text (392KB)] IEEE CNF

18 Floating-point nonlinear DSP coprocessor cell-two cycle chip

Jain, V.K.; Lin, L.,

OCÉANS '96. MTS/IEEE. 'Prospects for the 21st Century'. Conference Proceedings , 23-26 Sept. 1996

Pages: 133 suppl.

[Abstract] [PDF Full-Text (640KB)] IEEE CNF

19 On design of efficient square generator

Chin-Long Wey;

Computer Design: VLSI in Computers and Processors, 1996. ICCD '96. Proceedings., 1996 IEEE International Conference on , 7-9 Oct. 1996 Pages:506 - 511

[Abstract] [PDF Full-Text (620KB)] IEEE CNF

20 Floating-point nonlinear DSP coprocessor cell-two cycle chip

Jain, V.K.; Lin, L.;

VLSI Signal Processing, IX, 1996., [Workshop on], 30 Oct.-1 Nov. 1996 Pages:45 - 54

[Abstract] [PDF Full-Text (1456KB)] IEEE CNF

21 Novel reciprocal and square-root VLSI cell: architecture and application to signal processing

Jain, V.K.; Perez, G.E.; Wills, J.M.;

Acoustics, Speech, and Signal Processing, 1991. ICASSP-91., 1991 International

Conference on , 14-17 April 1991 Pages:1201 - 1204 vol.2

[Abstract] [PDF Full-Text (480KB)] IEEE CNF

22 An accurate, high speed implementation of division by reciprocal approximation

Fowler, D.L.; Smith, J.E.; Computer Arithmetic, 1989., Proceedings of 9th Symposium on , 6-8 Sept. 1989 Pages:60 - 67

[Abstract] [PDF Full-Text (580KB)] IEEE CNF

23 Efficient algorithms for binary logarithmic conversion and addition

Wan, Y.; Wey, C.L.;

Computers and Digital Techniques, IEE Proceedings-, Volume: 146, Issue: 3, May

Pages:168 - 172

[Abstract] [PDF Full-Text (272KB)] IEE JNL

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Pages:623 - 627

[Abstract] [PDF Full-Text (364KB)] IEEE JNL

2 Pre-layout delay calculation specification for CMOS ASIC libraries

Edamatsu, H.; Homma, K.; Kakimoto, M.; Koike, Y.; Tabuchi, K.;

Design Automation Conference 1998. Proceedings of the ASP-DAC '98. Asia and South

Pacific , 10-13 Feb. 1998

Pages:241 - 248

[Abstract] [PDF Full-Text (552KB)] IEEE CNF

3 Simplified motion planning strategies in flexible manufacturing

Mirolo, C.; Pagello, E.; Wen-Han Qian;

Assembly and Task Planning, 1995. Proceedings., IEEE International Symposium on 10-11 Aug. 1995

Pages:394 - 399

[Abstract] [PDF Full-Text (592KB)] IEEE CNF

4 On the use of scalar quantization for fast HMM computation

Sayayama, S.; Takahashi, S.;

Acoustics, Speech, and Signal Processing, 1995. ICASSP-95., 1995 International Conference on ,Volume: 1, 9-12 May 1995

Pages:213 - 216 vol.1

[Abstract] [PDF Full-Text (320KB)] IEEE CNF

5 Function evaluation by table look-up and addition

Hassler, H.: Takagi, N.:

Computer Arithmetic, 1995., Proceedings of the 12th Symposium on , 19-21 July 1995 Pages:10 - 16

[Abstract] [PDF Full-Text (516KB)] IEEE CNF

6 Cube-packing and two-level minimization

Murgai, R.; Brayton, R.K.; Sangiovanni-Vincentelli, A.; Computer-Aided Design, 1993. ICCAD-93. Digest of Technical Papers., 1993 IEEE/ACM International Conference on , 7-11 Nov. 1993 Pages:115 - 122

[Abstract] [PDF Full-Text (676KB)] IEEE CNF

7 Discrete cosine transform on irregular shape for image coding

Bi Mi; Cham Wai Kuen; Zheng Zhi Hang; TENCON '93. Proceedings. Computer, Communication, Control and Power Engineering 1993 IEEE Region 10 Conference on , Issue: 0 , 19-21 Oct. 1993 Pages:402 - 405 vol.3

[Abstract] [PDF Full-Text (228KB)] IEEE CNF

8 Genetic and reinforcement-based rule extraction for regulator control

Abu Zitar, R.A.; Hassoun, M.H.;

Decision and Control, 1993., Proceedings of the 32nd IEEE Conference on , 15-17 Dec. 1993

Pages:1258 - 1263 vol.2

[Abstract] [PDF Full-Text (324KB)] IEEE CNF

9 FLASH: a fast look-up algorithm for string homology

Califano, A.; Rigoutsos, I.;

Computer Vision and Pattern Recognition, 1993. Proceedings CVPR '93., 1993 IEEE Computer Society Conference on , 15-17 June 1993 Pages:353 - 359

[Abstract] [PDF Full-Text (652KB)] IEEE CNF

10 An accurate AC characteristic table look-up model for VLSI analog circuit simulation applications

Cho, D.-H.; Kang, S.M.;

Circuits and Systems, 1993., ISCAS '93, 1993 IEEE International Symposium on , 3-6 May 1993

Pages:1531 - 1534 vol.3

[Abstract] [PDF Full-Text (316KB)] IEEE CNF

11 Some results on the complexity of Boolean functions for table look up architectures

Murgai, R.; Brayton, R.K.; Sangiovanni-Vincentelli, A.;

Computer Design: VLSI in Computers and Processors, 1993. ICCD '93. Proceedings.,

1993 IEEE International Conference on , 3-6 Oct. 1993

Pages:505 - 512

[Abstract] [PDF Full-Text (620KB)] IEEE CNF

12 Performance directed synthesis for table look up programmable gate arrays

Murgai, R.; Shenoy, N.; Brayton, R.K.; Sangiovanni-Vincentelli, A.; Computer-Aided Design, 1991. ICCAD-91. Digest of Technical Papers., 1991 IEEE International Conference on , 11-14 Nov. 1991 Pages:572 - 575

[Abstract] [PDF Full-Text (392KB)] IEEE CNF

13 Improved logic synthesis algorithms for table look up architectures

Murgai, R.; Shenoy, N.; Brayton, R.K.; Sangiovanni-Vincentelli, A.; Computer-Aided Design, 1991. ICCAD-91. Digest of Technical Papers., 1991 IEEE International Conference on , 11-14 Nov. 1991

Pages:564 - 567

[Abstract] [PDF Full-Text (436KB)] IEEE CNF

14 The multiple switch-connected token passing ring networks

Jiunn-Hwa Yu; Wen-Tsuen Chen;

Local Computer Networks, 1991. Proceedings., 16th Conference on , 14-17 Oct. 1991 Pages:544 - 553

[Abstract] [PDF Full-Text (828KB)] IEEE CNF

15 Efficient computation of moments for pattern recognition

Wan-Chi Siu;

Communications, Computers and Signal Processing, 1991., IEEE Pacific Rim

Conference on , 9-10 May 1991

Pages:589 - 592 vol.2

[Abstract] [PDF Full-Text (276KB)] IEEE CNF

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16 A low-cost Gray prediction search chip for motion estimation

Jer Min Jou; Yeu-Horng Shiau; Pei-Yin Chen; Shiann-Rong Kuang; Circuits and Systems I: Fundamental Theory and Applications, IEEE Transactions on [see also Circuits and Systems I: Regular Papers, IEEE Transactions on], Volume: 49, Issue: 7, July 2002

Pages:928 - 938

[Abstract] [PDF Full-Text (467KB)] IEEE JNL

17 New bit-serial VLSI implementation of RNS FIR digital filters

Chin-Liang Wang;

Circuits and Systems II: Analog and Digital Signal Processing, IEEE Transactions on [see also Circuits and Systems II: Express Briefs, IEEE Transactions on], Volume: 41, Issue: 11, Nov. 1994

Pages:768 - 772

[Abstract] [PDF Full-Text (444KB)] IEEE JNL

18 Table look-up model of thin-film transistors for circuit simulation using spline interpolation with transformation by $y=x+\log(x)$

Kimura, M.; Inoue, S.; Shimoda, T.;

Computer-Aided Design of Integrated Circuits and Systems, IEEE Transactions on ,Volume: 21 , Issue: 9 , Sept. 2002

Pages:1101 - 1104

[Abstract] [PDF Full-Text (260KB)] IEEE JNL

19 Bit-estimate based decoding for vector quantization over noisy channels with intersymbol interference

Skogland, M.O.;

Communications, IEEE Transactions on ,Volume: 48 , Issue: 8 , Aug. 2000 Pages:1309 - 1317

[Abstract] [PDF Full-Text (204KB)] IEEE JNL

20 Design of high-speed simulation models for mobile fading channels by using table look-up techniques

Patzold, M.; Garcia, R.; Laue, F.;

Vehicular Technology, IEEE Transactions on ,Volume: 49 , Issue: 4 , July 2000

Pages:1178 - 1190

[Abstract] [PDF Full-Text (368KB)] IEEE JNL

21 Powering by a table look-up and a multiplication with operand modification

Takagi. N.:

Computers, IEEE Transactions on ,Volume: 47 , Issue: 11 , Nov. 1998

Pages:1216 - 1222

[Abstract] [PDF Full-Text (128KB)] IEEE JNL

22 An RNS Montgomery modular multiplication algorithm

Bajard, J.-C.; Didier, L.-S.; Kornerup, P.;

Computers, IEEE Transactions on ', Volume: 47 , Issue: 7 , July 1998

Pages:766 - 776

[Abstract] [PDF Full-Text (500KB)] IEEE JNL

23 Noise-predictive maximum likelihood (NPML) detection

Coker, J.D.; Eleftheriou, E.; Galbraith, R.L.; Hirt, W.;

Magnetics, IEEE Transactions on ,Volume: 34 , Issue: 1 , Jan. 1998

Pages:110 - 117

[Abstract] [PDF Full-Text (696KB)] IEEE JNL

24 Division algorithms and implementations

Obermann, S.F.; Flynn, M.J.; Computers, IEEE Transactions on ,Volume: 46 , Issue: 8 , Aug. 1997

Pages:833 - 854

[Abstract] [PDF Full-Text (240KB)] IEEE JNL

25 Nonlinear clutter cancellation and detection using a memory-based predictor

Aerospace and Electronic Systems, IEEE Transactions on ,Volume: 32 , Issue: 4 , Oct.

1996

Pages:1249 - 1256

[Abstract] [PDF Full-Text (804KB)] IEEE JNL

26 A mode-changeable 2-D DCT/IDCT processor for digital VCR

Seung-Kwon Paek; Ji-Heup Kim; Byung-Sup Kwon; Dae-Hyun Chung; Lee-Sup Kim; Consumer Electronics, IEEE Transactions on ,Volume: 42, Issue: 3, Aug. 1996 Pages:606 - 616

[Abstract] [PDF Full-Text (1024KB)] IEEE JNL

27 Decoding under integer metrics constraints

Salz, J.; Zehavi, E.;

Communications, IEEE Transactions on ,Volume: 43 , Issue: 234 , Feb./March/April

Pages:307 - 317

[Abstract] [PDF Full-Text (908KB)] IEEE JNL

28 Implementing multiplication with split read-only memory

Vinnakota, B.:

Computers, IEEE Transactions on ,Volume: 44 , Issue: 11 , Nov. 1995

Pages:1352 - 1356

[Abstract] [PDF Full-Text (404KB)] IEEE JNL

29 Neurocontrollers trained with rules extracted by a genetic assisted reinforcement learning system

Zitar, R.A.; Hassoun, M.H.;

Neural Networks, IEEE Transactions on ,Volume: 6 , Issue: 4 , July 1995

Pages:859 - 879

[Abstract] [PDF Full-Text (1544KB)] IEEE JNL

30 Nonlinear self-training adaptive equalization for partial-response systems *Cherubini, G.;*

Communications, IEEE Transactions on ,Volume: 42 , Issue: 234 , Feb-Apr 1994 Pages:367 - 376

[Abstract] [PDF Full-Text (824KB)] IEEE JNL

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31 On the application of sequence estimation algorithms in the digital compact cassette (DCC)

Braun, V.; Immink, K.A.S.; Ribeiro, M.A.; van den Enden, G.J.; Consumer Electronics, IEEE Transactions on ,Volume: 40 , Issue: 4 , Nov 1994 Pages:992 - 998

[Abstract] [PDF Full-Text (644KB)] IEEE JNL

32 Analysis of the convergence behavior of adaptive distributed-arithmetic echo cancellers

Cherubini, G.

Communications, IEEE Transactions on ,Volume: 41 , Issue: 11 , Nov. 1993 Pages:1703 - 1714

[Abstract] [PDF Full-Text (812KB)] IEEE JNL

33 Representations and routing for Cayley graphs [computer networks] Arden, B.W.; Tang, K.W.;

Communications, IEEE Transactions on ,Volume: 39 , Issue: 11 , Nov. 1991 Pages:1533 - 1537

[Abstract] [PDF Full-Text (404KB)] IEEE JNL

34 Serial architectures for the implementation of 2-D digital filters and for template matching in digital images

Sid-Ahmed, M.A.;

Acoustics, Speech, and Signal Processing [see also IEEE Transactions on Signal Processing], IEEE Transactions on ,Volume: 38 , Issue: 5 , May 1990 Pages:853 - 857

[Abstract] [PDF Full-Text (320KB)] IEEE JNL

35 Fault modeling of physical failures in CMOS VLSI circuits

Zaghloul, M.E.: Gobovic, D.,

Circuits and Systems, IEEE Transactions on ,Volume: 37 , Issue: 12 , Dec. 1990 Pages:1528 - 1543

[Abstract] [PDF Full-Text (1032KB)] IEEE JNL

36 Design and application of an optimizing XROM silicon compiler Linderman, R.W.; Rossbach, P.C.; Gallagher, D.M.;

Computer-Aided Design of Integrated Circuits and Systems, IEEE Transactions on ,Volume: 8 , Issue: 12 , Dec. 1989 Pages:1267 - 1275

[Abstract] [PDF Full-Text (1104KB)] IEEE JNL

37 Digital spectra of nonuniformly sampled signals. II. Digital look-up tunable sinusoidal oscillators

Jeng, Y.C.:

Instrumentation and Measurement, IEEE Transactions on ,Volume: 37, Issue: 3, Sept. 1988

Pages:358 - 362

[Abstract] [PDF Full-Text (352KB)] IEEE JNL

38 Multi-agent autonomic architecture and its application in e-medicine

Tianfield, H.;

Intelligent Agent Technology, 2003. IAT 2003. IEEE/WIC International Conference on 13-16 Oct. 2003

Pages:601 - 604

[Abstract] [PDF Full-Text (290KB)] IEEE CNF

39 Mechatronic design of innovative fingers for anthropomorphic robot hands

Biagiotti, L.; Lotti, F.; Melchiorri, C.; Vassura, G.;

Robotics and Automation, 2003. Proceedings. ICRA '03. IEEE International Conference on ,Volume: 3 , 14-19 Sept. 2003

Pages:3187 - 3192 vol.3

[Abstract] [PDF Full-Text (433KB)] IEEE CNF

40 Constructive pattern generation heuristic for meeting SSO limits

Baker, K.;

Test Conference, 2003. Proceedings. ITC 2003. International ,Volume: 2, Sept. 30,-Oct. 2. 2003

Pages:50 - 59

[Abstract] [PDF Full-Text (613KB)] IEEE CNF

41 Small signal point-to-point tracking of a propellant mixer

Barbieri, E.; Richter, H.; Figueroa, F.;

American Control Conference, 2003. Proceedings of the 2003, Volume: 4, 4-6 June

2003

Pages:2845 - 2850 vol.4

[Abstract] [PDF Full-Text (533KB)] IEEE CNF

42 Table look-up based compact modeling for on-chip interconnect timing and noise analysis

Haitian Hu; Blaauw, D.T.; Zolotov, V.; Gala, K.; Min Zhao; Panda, R.; Sapatnekar, S.S.; Circuits and Systems, 2003. ISCAS '03. Proceedings of the 2003 International Symposium on ,Volume: 4 , 25-28 May 2003 Pages:IV-668 - IV-671 vol.4

[Abstract] [PDF Full-Text (620KB)] IEEE CNF

43 An HMM-based speech recognition IC

Wei Han; Kwok-Wai Hon; Cheong-Fat Chan; Tan Lee; Chiu-Sing Choy; Kong-Pang Pun; Ching, P.C.

Circuits and Systems, 2003. ISCAS '03. Proceedings of the 2003 International Symposium on ,Volume: 2, 25-28 May 2003

Pages:II-744 - II-747 vol.2

[Abstract] [PDF Full-Text (338KB)] IEEE CNF

44 The simulation and concept of a pipe crawling robot for earthquake rescue

Zhelong Wang; Appleton, E.; Control Applications, 2002. Proceedings of the 2002 International Conference on ,Volume: 1, 18-20 Sept. 2002

Pages:339 - 344 vol.1

[Abstract] [PDF Full-Text (647KB)] IEEE CNF

45 Similarity classification for bio-signals using fuzzy table look-up by distance and eigenvector

Yoon, Y.R.; Shin, J.W.; Principe, J.;

[Engineering in Medicine and Biology, 2002. 24th Annual Conference and the Annual Fall Meeting of the Biomedical Engineering Society] EMBS/BMES Conference, 2002. Proceedings of the Second Joint ,Volume: 1, 2002

Pages:34 - 35 vol.1

[Abstract] [PDF Full-Text (234KB)] IEEE CNF

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JNL = Journal or Magazine CNF = Conference STD = Standard

46 Context-based adaptive binary arithmetic coding in JVT/H.26L

Marpe, D.; Schwarz, H.; Blattermann, G.; Heising, G.; Wiegand, T.; Image Processing. 2002. Proceedings. 2002 International Conference on ,Volume: 2

22-25 Sept. 2002 Pages:II-513 - II-516 vol.2

[Abstract] [PDF Full-Text (486KB)] IEEE CNF

47 Efficient conversion from binary to multi-digit multi-dimensional logarithmic number systems using arrays of range addressable look-up tables

Muscedere, R.; Dimitrov, V.S.; Jullien, G.A.; Miller, W.C.; Application-Specific Systems, Architectures and Processors, 2002. Proceedings. The IEEE International Conference on , 17-19 July 2002

Pages:130 - 138

[Abstract] [PDF Full-Text (260KB)] IEEE CNF

48 Finitely parameterised implementation of receding horizon control for constrained linear systems

Seron, M.M.; Goodwin, G.C.; De Dona, J.A.;

American Control Conference, 2002. Proceedings of the 2002, Volume: 6, 8-10 May 2002

Pages:4481 - 4486 vol.6

[Abstract] [PDF Full-Text (540KB)] IEEE CNF

49 Fuzzy modeling of human control strategy for over head crane

Akbarzadeh-T, M.-R.; Meghdadi, A.H.;

Fuzzy Systems, 2001. The 10th IEEE International Conference on ,Volume: 3 , 2-5 Dec. 2001

Pages:1076 - 1079

[Abstract] [PDF Full-Text (570KB)] IEEE CNF

50 Defect-tolerance design of the high-speed RSA encryption processor with built-in table for residue calculation of redundant binary numbers

Tomabechi, N.; Ito, T.;

Electronics, Circuits and Systems, 2001. ICECS 2001. The 8th IEEE International

Conference on ,Volume: 1 , 2-5 Sept. 2001 Pages:267 - 271 vol.1

[Abstract] [PDF Full-Text (328KB)] IEEE CNF

51 Faithful powering computation using table look-up and a fused accumulation tree

Pineiro, J.A.; Bruguera, J.D.; Muller, J.M.; Computer Arithmetic, 2001. Proceedings. 15th IEEE Symposium on , 11-13 June 2001 Pages:40 - 47

[Abstract] [PDF Full-Text (780KB)] IEEE CNF

52 Accurate power macro-modeling techniques for complex RTL circuits Potlapally, N.R.; Raghunathan, A.; Lakshminarayana, G.; Hsiao, M.S.; Chakradhar, S.T.; VLSI Design, 2001. Fourteenth International Conference on , 3-7 Jan. 2001 Pages:235 - 241

[Abstract] [PDF Full-Text (644KB)] IEEE CNF

53 On weight initialization for adaptive multistage parallel interference cancellation in CDMA systems

Chin-Liang Wang; Kuo-Ming Wu; Ming-Shiang Lai; Wireless Communications, 2001. (SPAWC '01). 2001 IEEE Third Workshop on Signal Processing Advances in , 20-23 March 2001 Pages: 194 - 197

[Abstract] [PDF Full-Text (272KB)] IEEE CNF

54 Fuzzy modeling of nonlinear stochastic systems by learning from examples Meghdadi, A.H.; Akbarzadeh-T, M.R.;

IFSA World Congress and 20th NAFIPS International Conference, 2001. Joint 9th , 25-28 July 2001

Pages:2746 - 2751 vol.5

[Abstract] [PDF Full-Text (372KB)] IEEE CNF

55 FPGA implementation of a faithful polynomial approximation for powering function computation

Pineiro, J.A.; Bruguera, J.D.; Muller, J.M.;

Digital Systems, Design, 2001. Proceedings. Euromicro Symposium on , 4-6 Sept. 2001 Pages:262 - 269

[Abstract] [PDF Full-Text (464KB)] IEEE CNF

56 Frequency estimation utilizing the Hadamard transform

Andersson, T.; Skoglund, M.; Handel, P.;

Statistical Signal Processing, 2001. Proceedings of the 11th IEEE Signal Processing Workshop on , 6-8 Aug. 2001

Pages:409 - 412

[Abstract] [PDF Full-Text (304KB)] IEEE CNF

57 Design of a high-speed RSA encryption processor with built-in table for residue calculation of redundant binary numbers

Tomabechi, N.; Ito, T.;

Circuits and Systems, 2000. Proceedings. ISCAS 2000 Geneva. The 2000 IEEE International Symposium on ,Volume: 5 , 28-31 May 2000 Pages:697 - 700 vol.5

[Abstract] [PDF Full-Text (232KB)] IEEE CNF

58 Byte-oriented decoding of canonical Huffman codes

Nekritch, Y.;

Information Theory, 2000. Proceedings. IEEE International Symposium on , 25-30 June 2000

Pages:371

[Abstract] [PDF Full-Text (100KB)] IEEE CNF

59 Inverse error diffusion using table look-up and vector quantization: a novel scheme for low bit rate

Dujmic, H.; Rozic, N.; Ursic, J.;

Acoustics, Speech, and Signal Processing, 2000. ICASSP '00. Proceedings. 2000 IEEE International Conference on ,Volume: 6, 5-9 June 2000

Pages:1899 - 1902 vol.4

[Abstract] [PDF Full-Text (616KB)] IEEE CNF

60 Chip design of mel frequency cepstral coefficients for speech recognition *Jia-Ching Wang; Jhing-Fa Wang; Yu-Sheng Weng;*Acoustics, Speech, and Signal Processing, 2000. ICASSP '00. Proceedings. 2000 IEEE International Conference on ,Volume: 6 , 5-9 June 2000 Pages:3658 - 3661 vol.6

[Abstract] [PDF Full-Text (388KB)] IEEE CNF

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61 Hash parallel and label parallel routing for high performance multicast router with fine grain QoS control

Ohta, M.; Sola, M.; Fujikawa, K.; Kojima, A.; Fukumori, H.; Muraoka, Y.; Internet Workshop, 1999. IWS 99, 18-20 Feb. 1999 Pages:13 - 16

[Abstract] [PDF Full-Text (288KB)] IEEE CNF

62 A radix-2 general division algorithm with carry-free scheme and the divider implementation

Jen-Shiun Chiang; Hung-Da Chung; Ming-Hsou Tsai; Electronics, Circuits and Systems, 1999. Proceedings of ICECS '99. The 6th IEEE International Conference on ,Volume: 1 , 5-8 Sept. 1999 Pages:569 - 572 vol.1

[Abstract] [PDF Full-Text (260KB)] IEEE CNF

63 A fast and compact longest match prefix look-up method using pointer cache for very long network address

Uga, M.; Shiomoto, K.;

Computer Communications and Networks, 1999. Proceedings. Eight International Conference on , 11-13 Oct. 1999 Pages:595 - 602

[Abstract] [PDF Full-Text (668KB)] IEEE CNF

64 Generic implementation of DD packages in MVL

Drechsler, R.; Jankovic, D.; Stankovic, R.S.;

EUROMICRÓ Conference, 1999. Proceedings. 25th ,Volume: 1 , 8-10 Sept. 1999 Pages:352 - 359 vol.1

[Abstract] [PDF Full-Text (120KB)] IEEE CNF

65 TETA: transistor-level engine for timing analysis

Dartu, F.; Pileggi, L.T.;

Design Automation Conference, 1998. Proceedings, 15-19 June 1998

Pages:595 - 598

[Abstract] [PDF Full-Text (396KB)] IEEE CNF

66 Two improved algorithms and hardware implementations for key distribution

using extended programmable cellular automata

Zhang, C.N.; Deng, M.; Mason, R.;

Computer Security Applications Conference, 1998, Proceedings., 14th Annual, 7-11

Dec. 1998 Pages:244 - 249

[Abstract] [PDF Full-Text (180KB)] IEEE CNF

67 Watermarking for image authentication

Min Wu; Bede Liu;

Image Processing, 1998. ICIP 98. Proceedings. 1998 International Conference on

,Volume: 2 , 4-7 Oct. 1998

Pages:437 - 441 vol.2

[Abstract] [PDF Full-Text (548KB)] IEEE CNF

68 On nonlinear utilization of intervector dependency in vector quantization

Skoglund, M.; Skoglund, J.;

Acoustics, Speech, and Signal Processing, 1998. ICASSP '98. Proceedings of the 1998 IEEE International Conference on ,Volume: 1 , 12-15 May 1998 Pages:361 - 364 vol.1

[Abstract] [PDF Full-Text (424KB)] IEEE CNF

69 Speech enhancement based on minimum mean-square error short-time spectral estimation and its realization

Liu Zhibin; Xu Naiping;

Intelligent Processing Systems, 1997. ICIPS '97. 1997 IEEE International Conference on

,Volume: 2 , 28-31 Oct. 1997 Pages:1794 - 1797 vol.2

[Abstract] [PDF Full-Text (360KB)] IEEE CNF

70 An RNS Montgomery modular multiplication algorithm

Bajard, J.-C.; Didier, L.-S.; Kornerup, P.;

Computer Arithmetic, 1997. Proceedings., 13th IEEE Symposium on , 6-9 July 1997

Pages:234 - 239

[Abstract] [PDF Full-Text (372KB)] IEEE CNF

71 Generating a power of an operand by a table look-up and a multiplication

Takagi, N.:

Computer Arithmetic, 1997. Proceedings., 13th IEEE Symposium on , 6-9 July 1997

Pages:126 - 131

[Abstract] [PDF Full-Text (428KB)] IEEE CNF

72 Noise-predictive maximum-likelihood (NPML) detection for the magnetic recording channel

Eleftheriou, E.; Hirt, W.;

Communications, 1996. ICC 96, Conference Record, Converging Technologies for Tomorrow's Applications. 1996 IEEE International Conference on ,Volume: 1, 23-27 June 1996

Pages:556 - 560 vol.1

[Abstract] [PDF Full-Text (472KB)] IEEE CNF

73 An on line adaptive data compression chip using arithmetic codes Jer-Min Jou; Shiann-Rong Kuang; Yuh-Lin Chen; Chung-Yuan Chiang; Circuits and Systems, 1996. ISCAS '96., 'Connecting the World'., 1996 IEEE International Symposium on ,Volume: 4 , 12-15 May 1996 Pages:360 - 363 vol.4

[Abstract] [PDF Full-Text (408KB)] IEEE CNF

74 Adaptive piecewise linear bits estimation model for MPEG based video coding

Jia-Bao Cheng; Hsueh-Ming Hang;

Image Processing, 1995. Proceedings., International Conference on ,Volume: 2, 23-26 Oct. 1995

Pages:551 - 554 vol.2

[Abstract] [PDF Full-Text (352KB)] IEEE CNF

75 A hippocampal model implementation using VLSI table-look-up and model-based approaches

Tsai, R.H.; Chou, E.Y.; Sheu, B.J.; Berger, T.W.; Neural Networks, 1995. Proceedings., IEEE International Conference on ,Volume: 3, 27

Nov.-1 Dec. 1995

Pages:1508 - 1512 vol.3

[Abstract] [PDF Full-Text (364KB)] IEEE CNF

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76 An analysis on the realisation of two-dimensional image moments

Siu, W.-C.; Lau, A.W.-F.; Wong, W.-H.; Lam, K.-M.;

Circuits and Systems, 1992. ISCAS '92. Proceedings., 1992 IEEE International

Symposium on ,Volume: 2, 3-6 May 1992

Pages:722 - 725 vol.2

[Abstract] [PDF Full-Text (308KB)] IEEE CNF

77 Sequential synthesis for table look up PGAs

Murgai, R.; Brayton, R.K.; Sangiovanni-Vincentelli, A.;

Euro ASIC '92, Proceedings. , 1-5 June 1992

Pages:32 - 37

[Abstract] [PDF Full-Text (476KB)] IEEE CNF

78 Area and delay mapping for table-look-up based field programmable gate arrays

Sawkar, P.; Thomas, D.;

Design Automation Conference, 1992. Proceedings., 29th ACM/IEEE, 8-12 June 1992 Pages:368 - 373

[Abstract] [PDF Full-Text (452KB)] IEEE CNF

79 Computer-aided design of avionic diagnostics algorithms

Hammett, R.;

Aerospace and Electronics Conference, 1992. NAECON 1992., Proceedings of the IEEE 1992 National, 18-22 May 1992

Pages:948 - 954 vol.3

[Abstract] [PDF Full-Text (580KB)] IEEE CNF

80 A new wake-potential calculation method using orthogonal polynomials

Wang, T.-S.F.; Zotter, B.;

Particle Accelerator Conference, 1991. 'Accelerator Science and Technology'., Conference Record of the 1991 IEEE, 6-9 May 1991

Pages:491 - 493 vol.1

[Abstract] [PDF Full-Text (216KB)] IEEE CNF

81 A high speed BiCMOS table look-up gate

Baltus, P.; van der Meulen, P.S.; Ligthart, M.;

Custom Integrated Circuits Conference, 1991., Proceedings of the IEEE 1991, 12-15 May 1991 Pages:6.3/1 - 6.3/4

[Abstract] [PDF Full-Text (408KB)] IEEE CNF

82 Mixed-mode incremental simulation and concurrent fault simulation

Ju, Y.-C.; Yang, F.L.; Saleh, R.A.;

Computer-Aided Design, 1990. ICCAD-90. Digest of Technical Papers., 1990 IEEE International Conference on , 11-15 Nov. 1990

Pages:158 - 161

[Abstract] [PDF Full-Text (380KB)] IEEE CNF

83 Rapid clock recovery technique with monotonic phase error metric

Maples, B.W.; Dapper, M.J.; Hill, T.J.;

Military Communications Conference, 1990. MILCOM '90, Conference Record, 'A New Era'. 1990 IEEE , 30 Sept.-3 Oct. 1990

Pages:141 - 145 vol.1

[Abstract] [PDF Full-Text (412KB)] IEEE CNF

84 The one over eight squared algorithm: a new way for computing convolutions and complex multiplications

Skavantzos, A.;

Circuits and Systems, 1990., IEEE International Symposium on , 1-3 May 1990 Pages:61 - 64 vol.1

[Abstract] [PDF Full-Text (260KB)] IEEE CNF

85 Lower bounds on the MSE of the single and double loop sigma delta modulators

Hein, S.; Zakhor, A.;

Circuits and Systems, 1990., IEEE International Symposium on , 1-3 May 1990 Pages:1751 - 1755 vol.3

[Abstract] [PDF Full-Text (420KB)] IEEE CNF

86 A table look-up model using a 3-D isoparametric shape function with improved convergency

Dae-Hyung Cho; Tae-Han Kim; Jeong-Taek Kong;

Computer-Aided Design, 1989. ICCAD-89. Digest of Technical Papers., 1989 IEEE International Conference on , 5-9 Nov. 1989

Pages:244 - 247

[Abstract] [PDF Full-Text (216KB)] IEEE CNF

87 Nonlinear adaptive filtering for echo cancellation

Michaelides, J.F.; Kabal, P.; Communications, 1988. ICC 88. Digital Technology - Spanning the Universe. Conference Record. IEEE International Conference on , 12-15 June 1988 Pages:951 - 956 vol.2

[Abstract] [PDF Full-Text (372KB)] IEEE CNF

88 Efficient encoding and decoding algorithms for variable-length entropy codes

Higgie, G.R.; Fong, A.C.M.;

Communications, IEE Proceedings-, Volume: 150, Issue: 5, 14 Oct. 2003

Pages:305-11

[Abstract] [PDF Full-Text (268KB)] IEE JNL

89 Hierarchical vector quantisation [image compression]

Luttrell, S.P.;

Communications, Speech and Vision, IEE Proceedings I [see also IEE Proceedings-Communications], Volume: 136, Issue: 6, Dec. 1989 Pages: 405 - 413

[Abstract] [PDF Full-Text (596KB)] IEE JNL

90 Bit-serial modular multiplier

Tomlinson, A.;

Electronics Letters , Volume: 25 , Issue: 24 , 23 Nov. 1989

Pages:1664

[Abstract] [PDF Full-Text (92KB)] IEE JNL

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